

REPORT 2200E

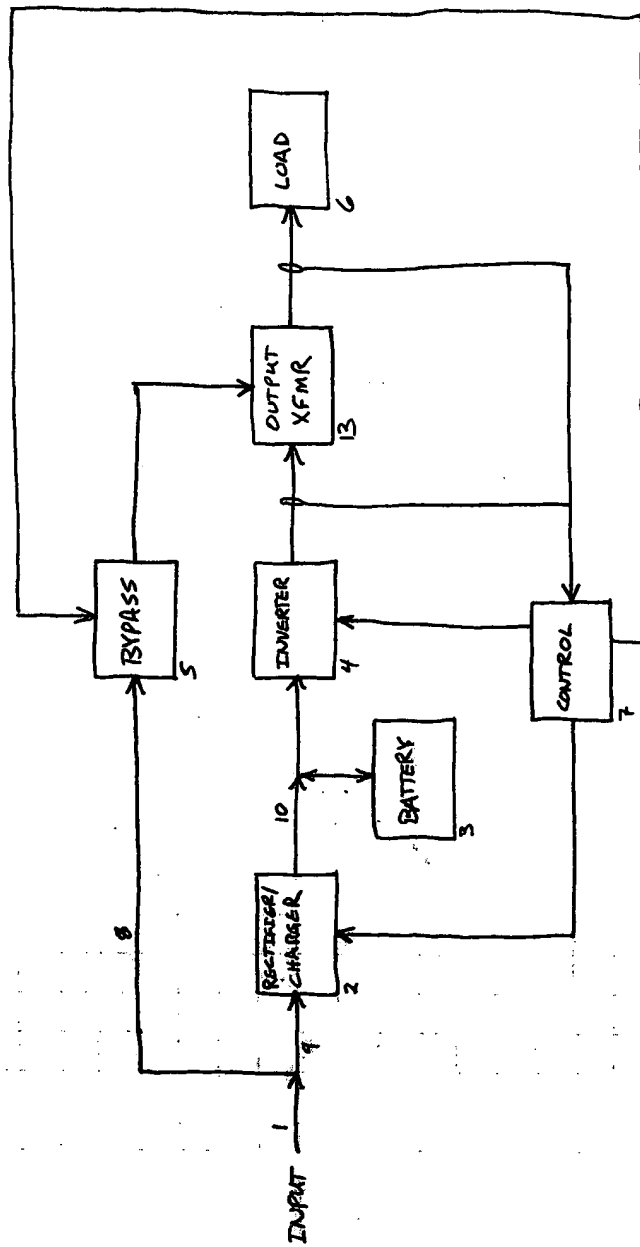


Fig. 1

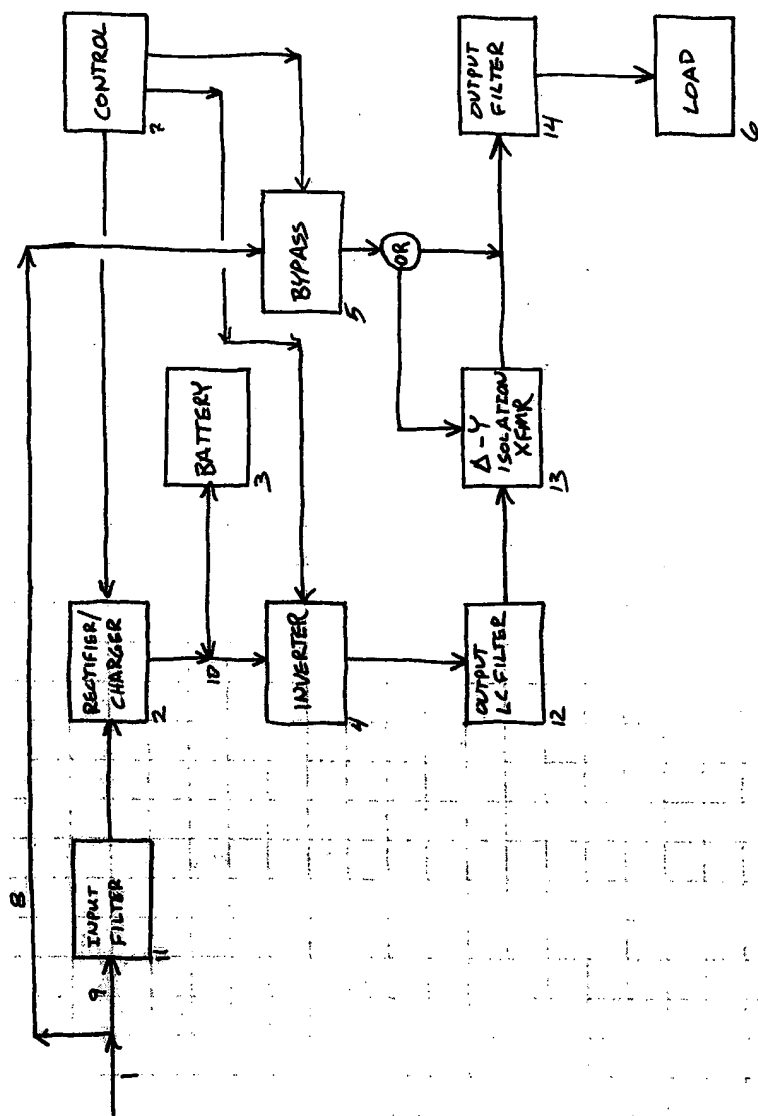


Fig. 2

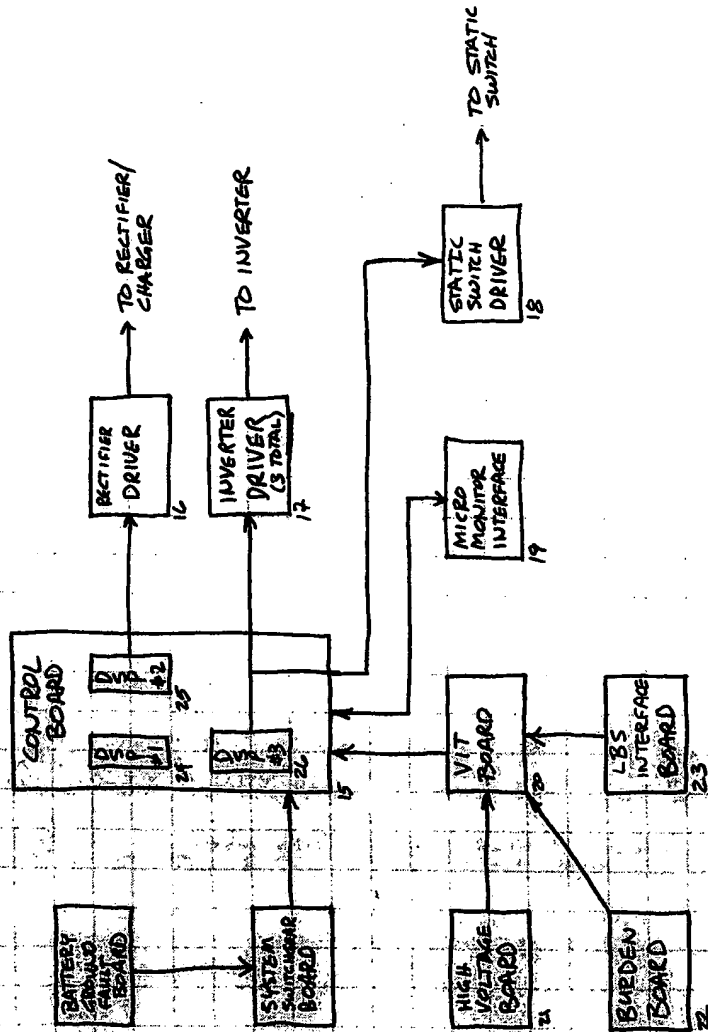


Fig. 3

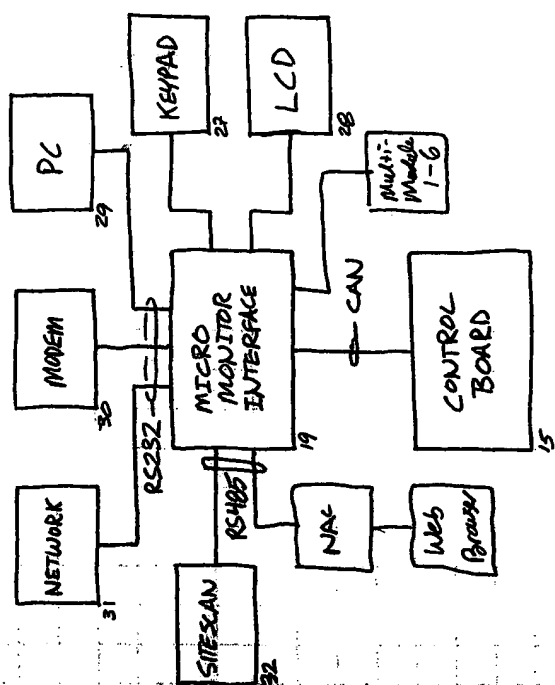


Fig. 4

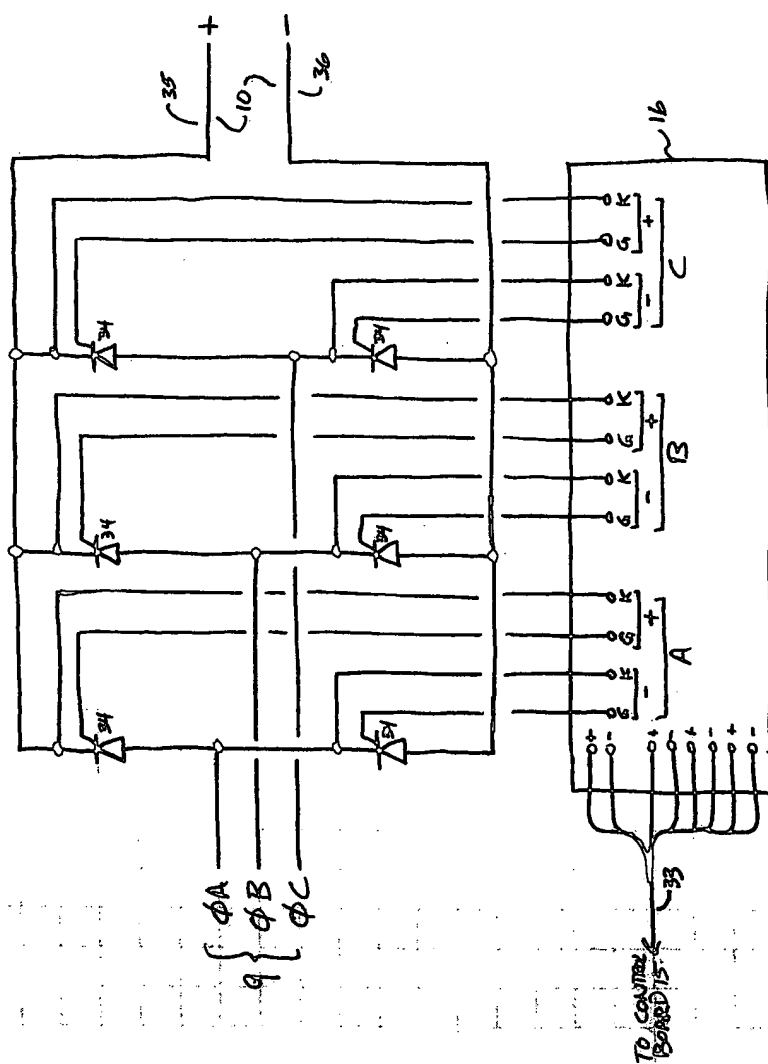
$$\left\{ \begin{array}{l} \phi A \\ \phi B \\ \phi C \end{array} \right.$$


Fig. 5

Year	1970	1971	1972	1973	1974	1975	1976	1977	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100
1970	1970	1971	1972	1973	1974	1975	1976	1977	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100

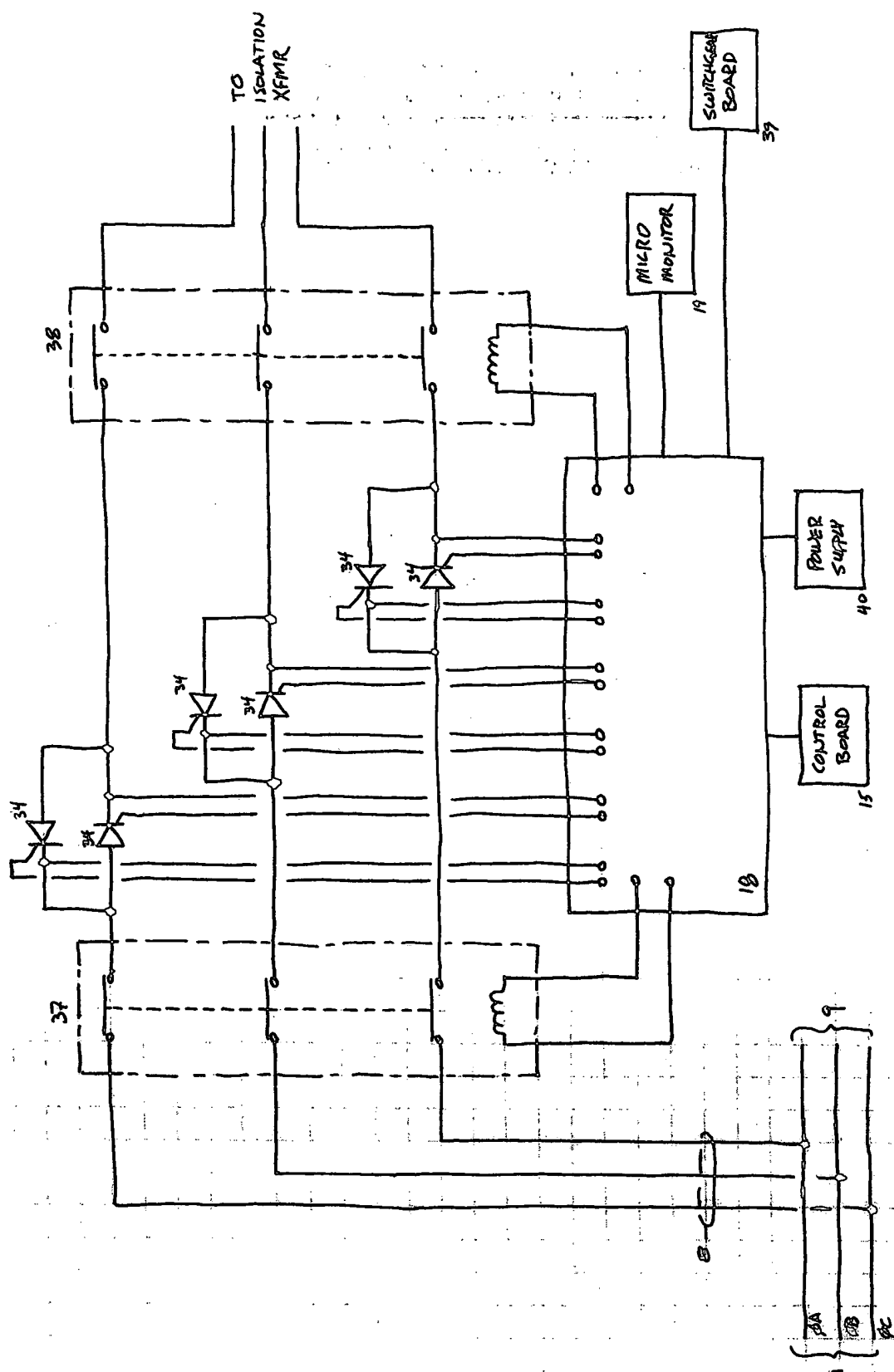


Fig. 6

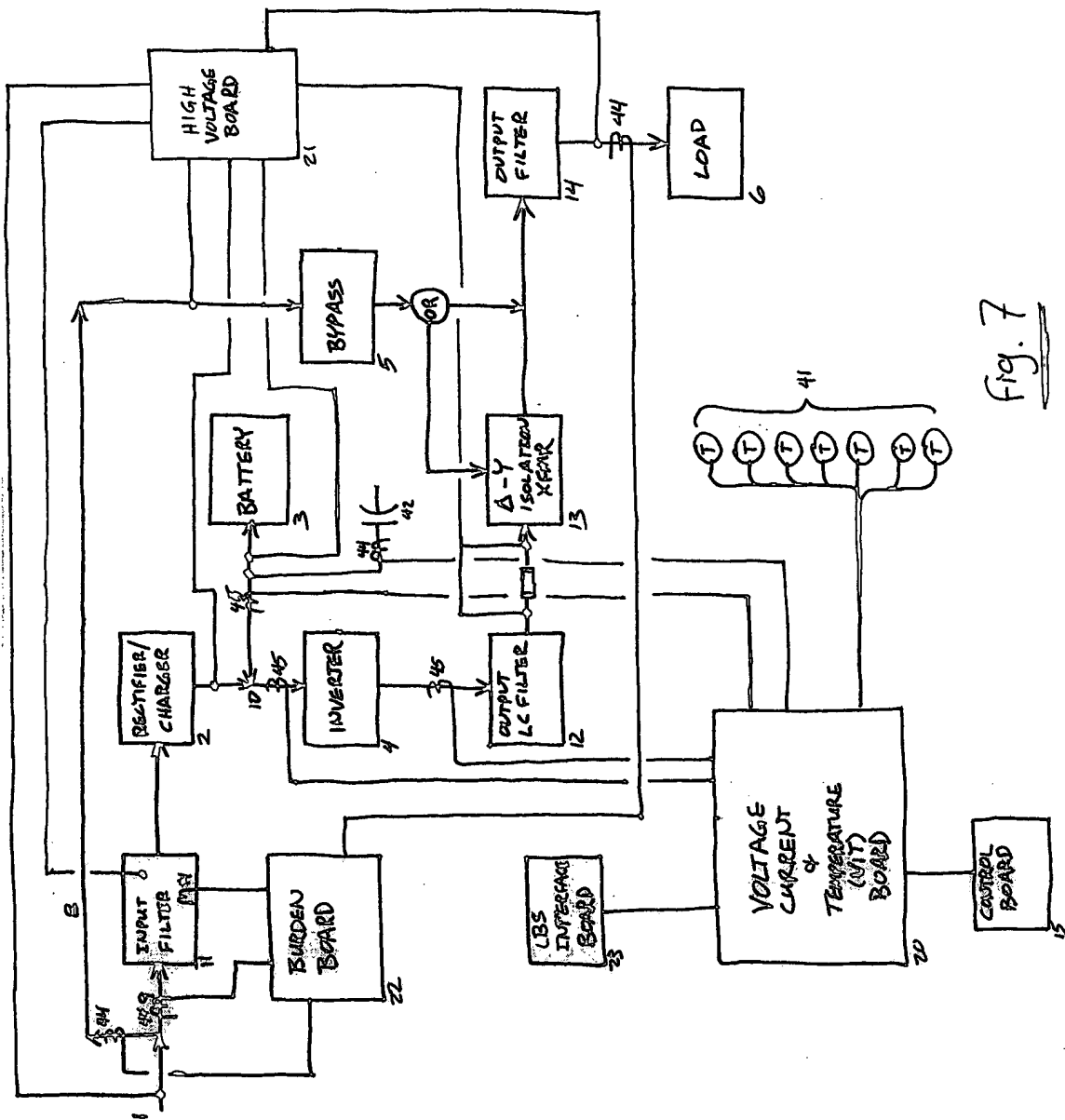


Fig. 7

Close SBS Contactors Flow Chart

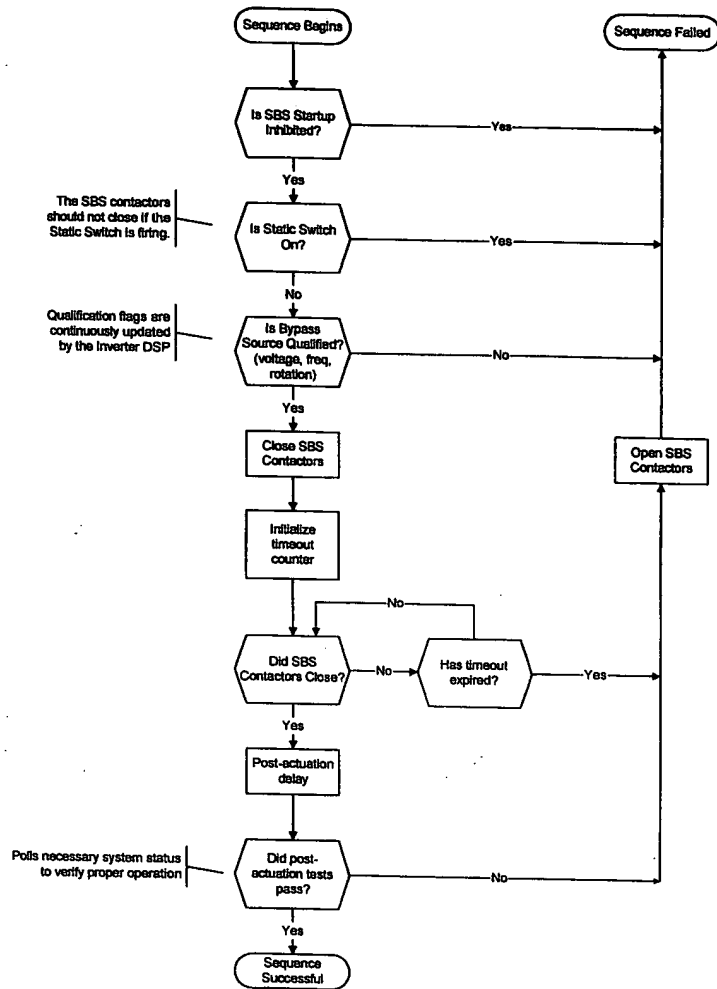


Fig.8

DSP Power-Up
Flow Chart

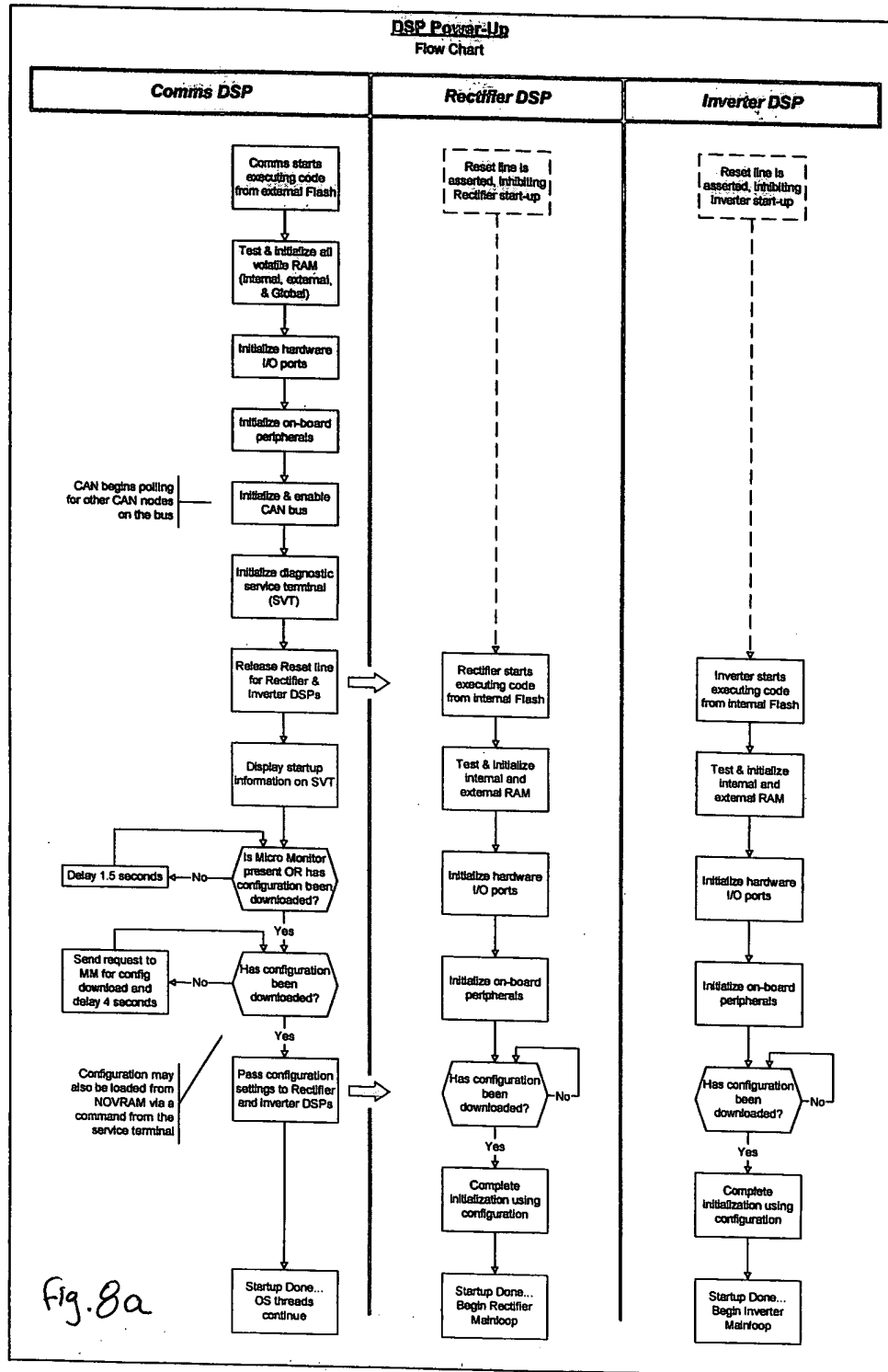


Fig. 8a

Start SBS
Flow Chart

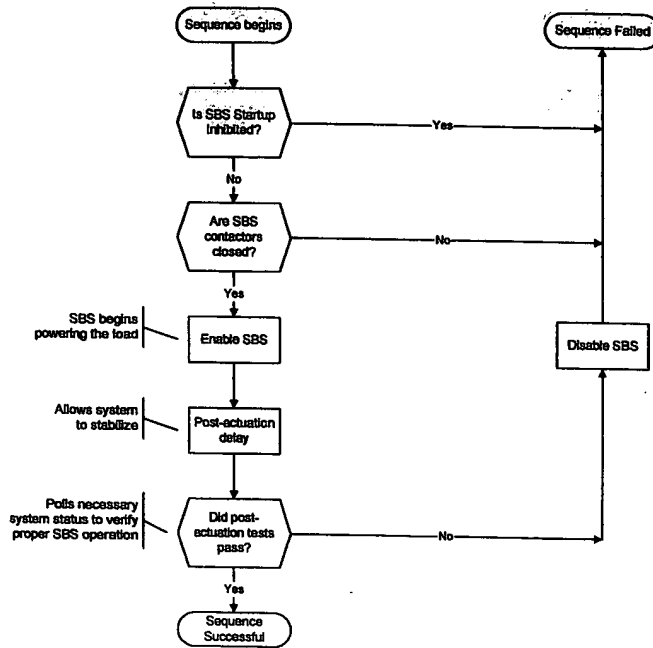


Fig. 9

Close Input Contactor
Flow Chart

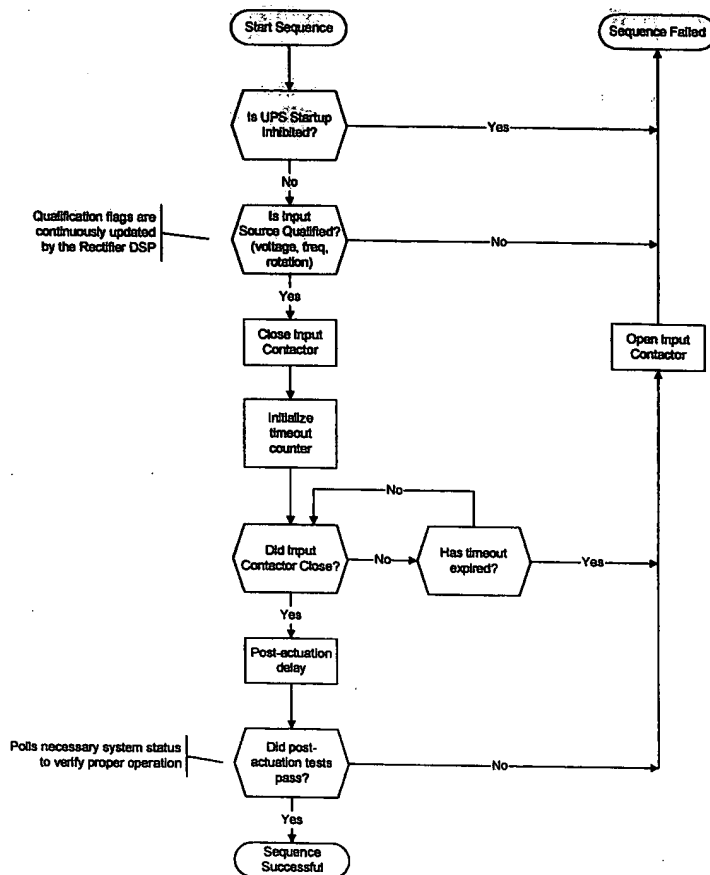


Fig. 10

Start Rectifier Flow Chart

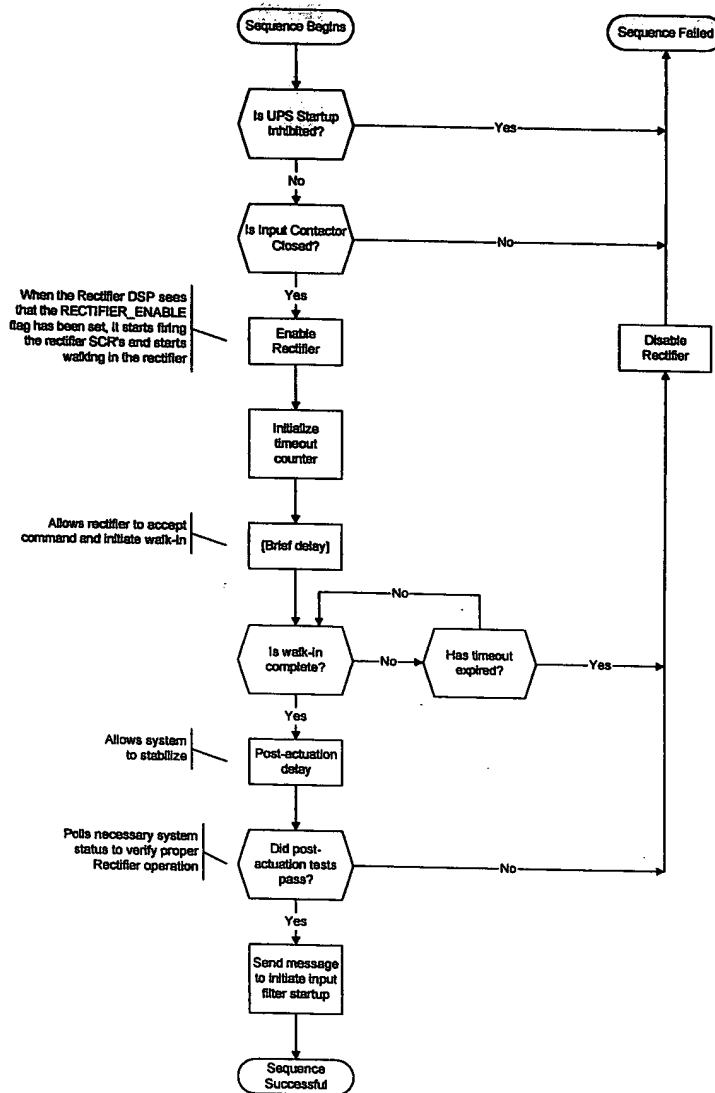


Fig. 11

Start Input Filter
Flow Chart

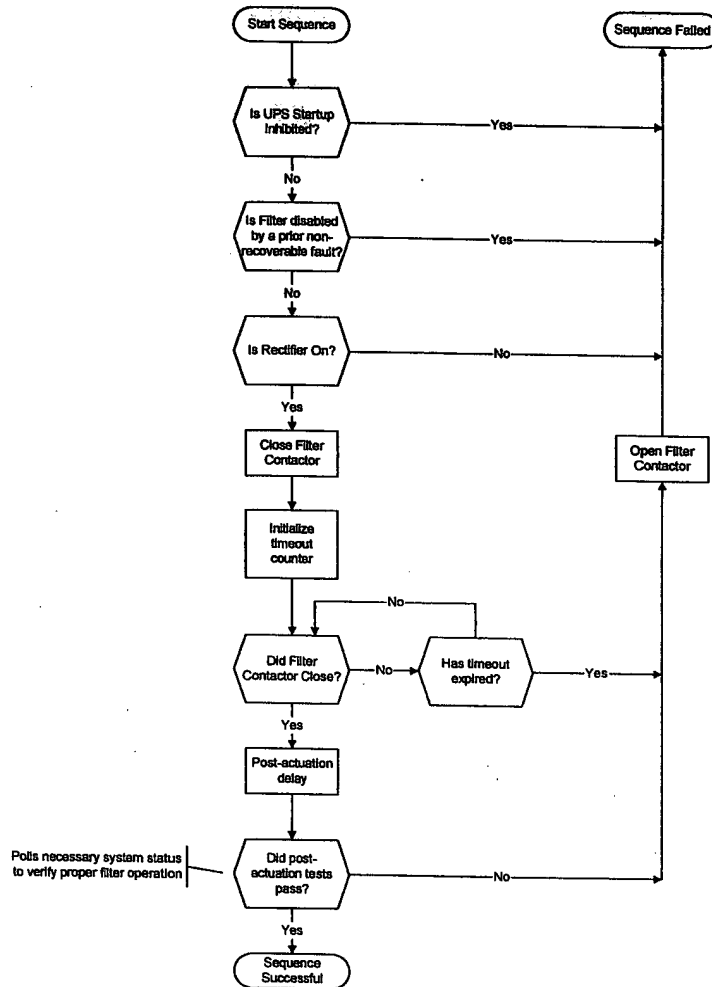
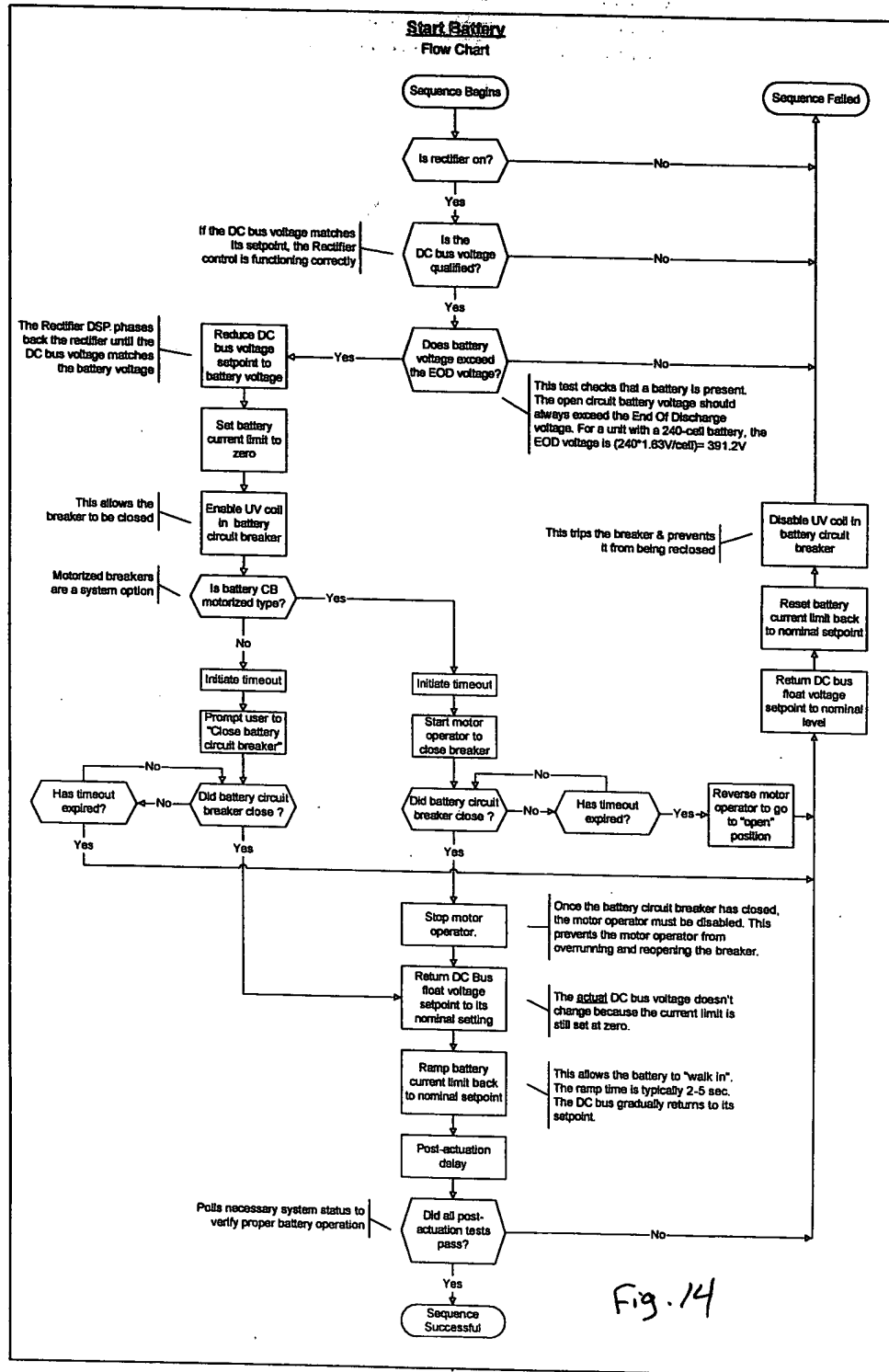


Fig. 12



Start Inverter Flow Chart

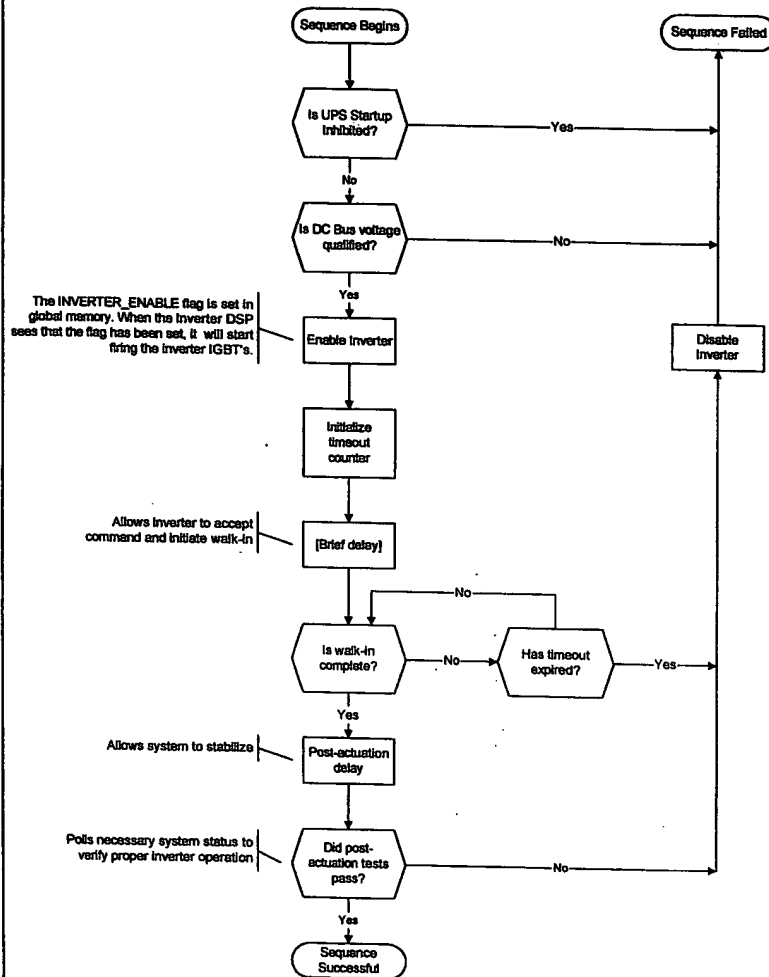


Fig. 15

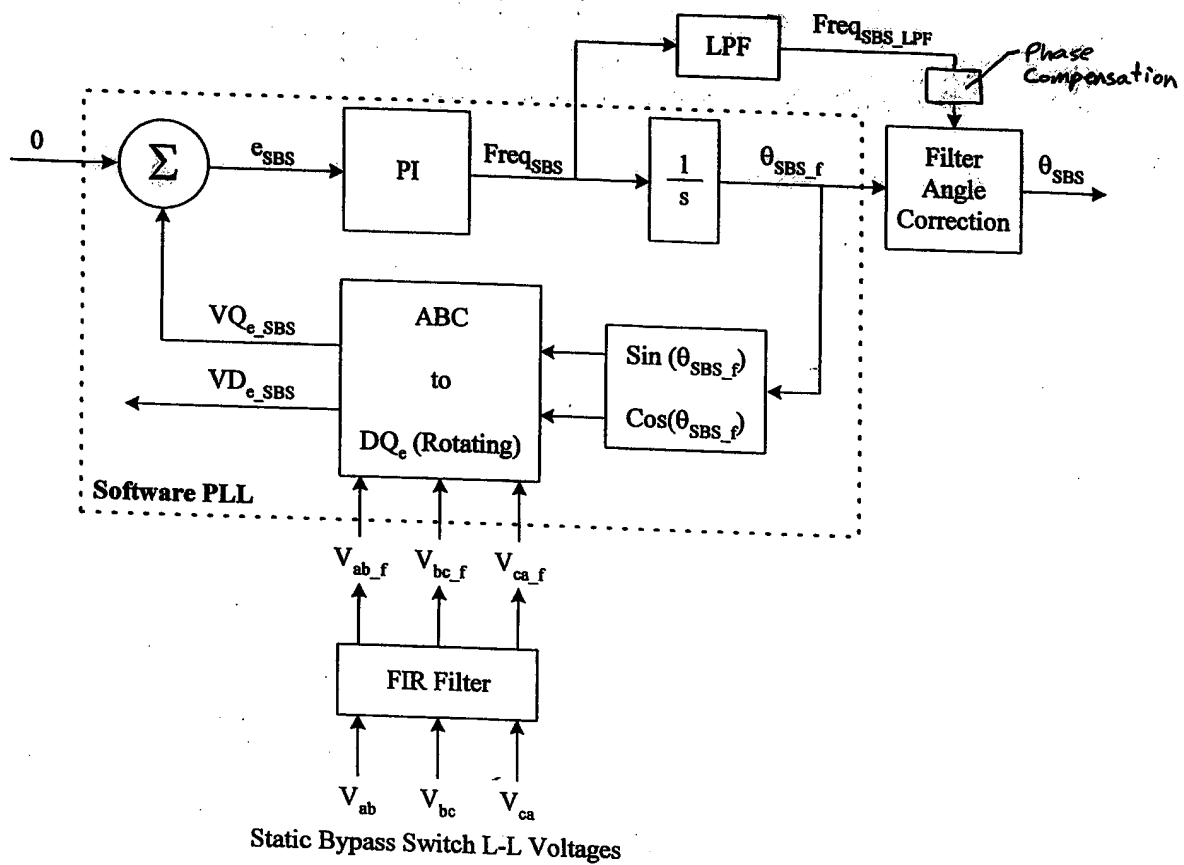


Fig. 16

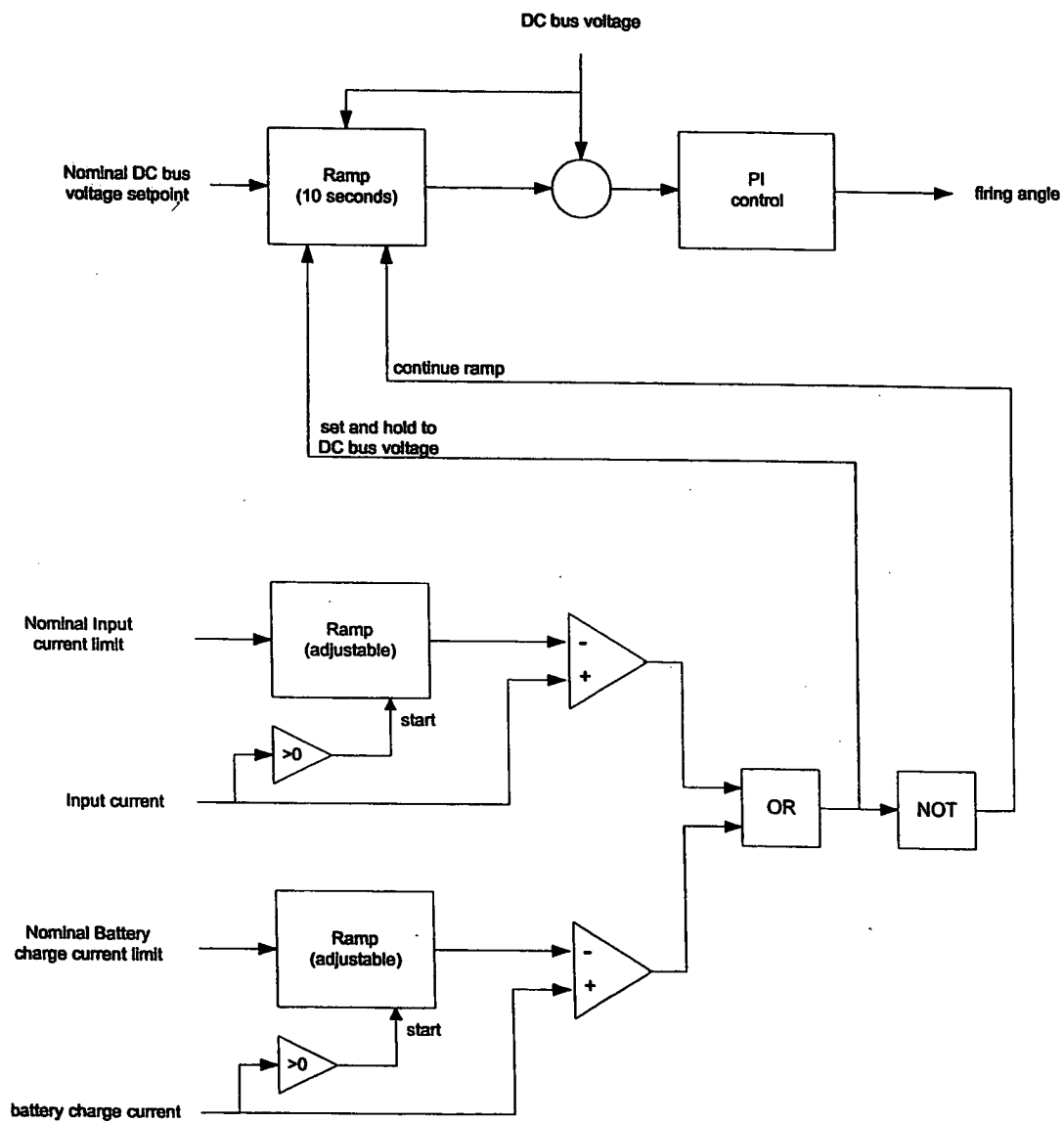


Fig. 17a

Rectifier Control Block Diagram

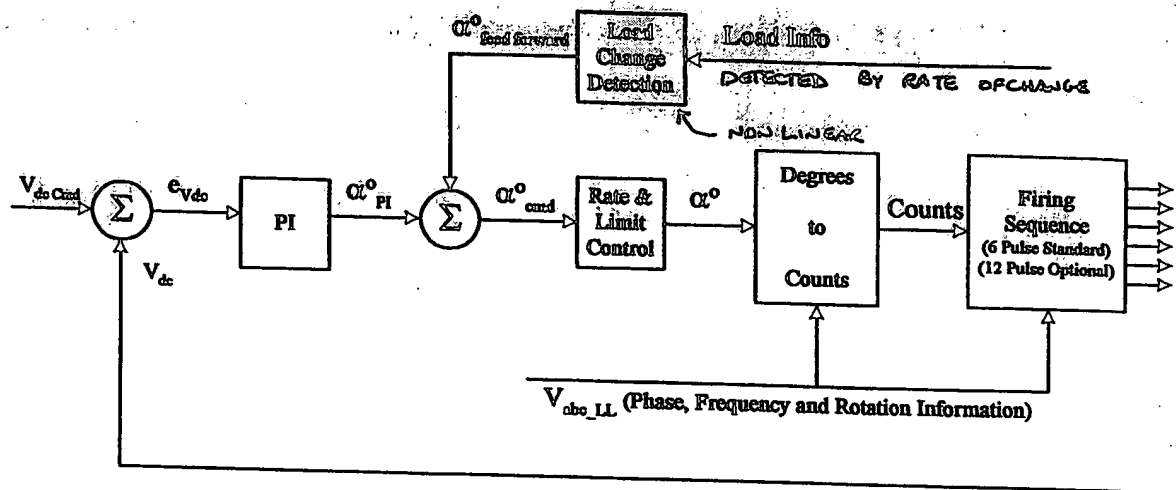
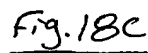
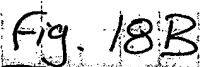


Fig. 17b



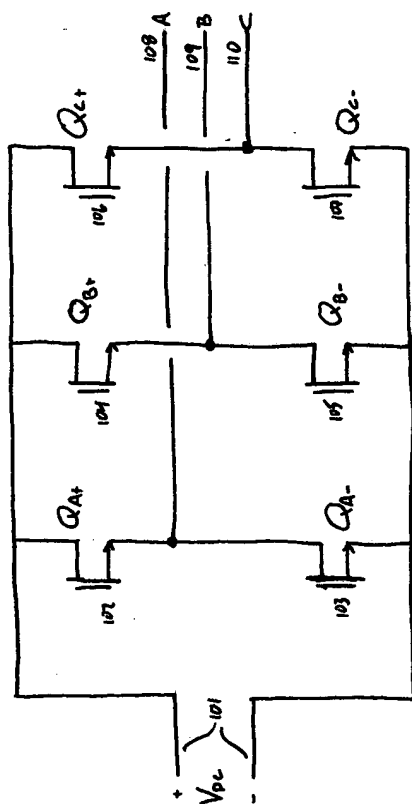
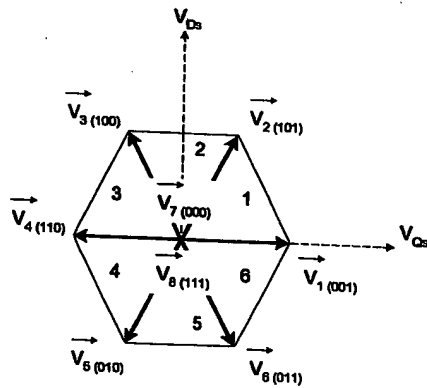


Fig. 19

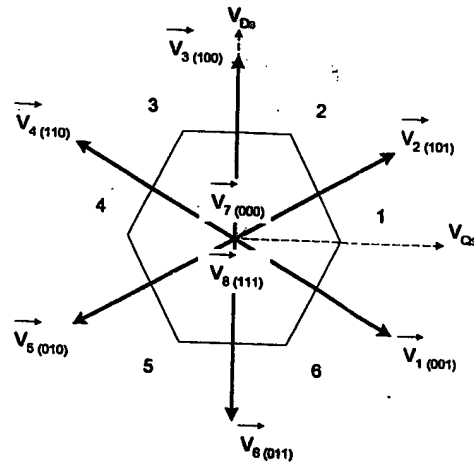
Switch (0 = OFF, 1 = ON)			Line to Neutral Voltage Vectors						Line to Line Voltage Vectors				
S_{C^*}	S_{B^*}	S_{A^*}	V_{AN}	V_{BN}	V_{CN}	$V = [V_{C^*} \ V_{B^*}]^T$	Vector		V_{AB}	V_{BC}	V_{CA}	$V = [V_{C^*} \ V_{B^*}]^T$	Vector
0	0	1	$2/3 V_{dc}$	$-1/3 V_{dc}$	$-1/3 V_{dc}$	$2/3 V_{dc} \angle 0^\circ$	\vec{V}_1		V_{dc}	0	$-V_{dc}$	$2/\sqrt{3} V_{dc} \angle -30^\circ$	\vec{V}_1
1	0	1	$1/3 V_{dc}$	$-2/3 V_{dc}$	$1/3 V_{dc}$	$2/3 V_{dc} \angle 60^\circ$	\vec{V}_2		V_{dc}	$-V_{dc}$	0	$2/\sqrt{3} V_{dc} \angle 30^\circ$	\vec{V}_2
1	0	0	$-1/3 V_{dc}$	$-1/3 V_{dc}$	$2/3 V_{dc}$	$2/3 V_{dc} \angle 120^\circ$	\vec{V}_3		0	$-V_{dc}$	V_{dc}	$2/\sqrt{3} V_{dc} \angle 90^\circ$	\vec{V}_3
1	1	0	$-2/3 V_{dc}$	$1/3 V_{dc}$	$1/3 V_{dc}$	$2/3 V_{dc} \angle 180^\circ$	\vec{V}_4		$-V_{dc}$	0	V_{dc}	$2/\sqrt{3} V_{dc} \angle 150^\circ$	\vec{V}_4
0	1	0	$-1/3 V_{dc}$	$2/3 V_{dc}$	$-1/3 V_{dc}$	$2/3 V_{dc} \angle 240^\circ$	\vec{V}_5		$-V_{dc}$	V_{dc}	0	$2/\sqrt{3} V_{dc} \angle 210^\circ$	\vec{V}_5
0	1	1	$1/3 V_{dc}$	$1/3 V_{dc}$	$-2/3 V_{dc}$	$2/3 V_{dc} \angle 300^\circ$	\vec{V}_6		0	V_{dc}	$-V_{dc}$	$2/\sqrt{3} V_{dc} \angle 270^\circ$	\vec{V}_6
0	0	0	0	0	0	0	\vec{V}_7		0	0	0	0	\vec{V}_7
1	1	1	0	0	0	0	\vec{V}_8		0	0	0	0	\vec{V}_8

Possible Switch Combinations (note: 0 = Switch OFF, 1 = Switch ON), Equivalent Line to Neutral Voltage Vectors and Equivalent Line to Line Voltage Vectors.

Fig. 20

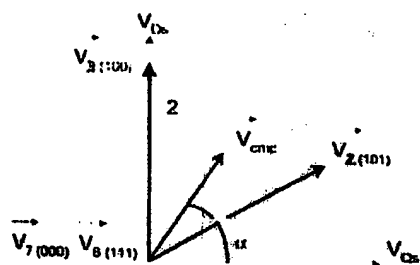


Line to Neutral Voltage Vectors Projected Onto The DQs Axis (Note: (001) = S_{C^*} OFF, S_{B^*} OFF, S_{A^*} ON)



Line to Line Voltage Vectors Projected Onto The DQs Axis (Note: (001) = S_{C^*} OFF, S_{B^*} OFF, S_{A^*} ON)

Fig. 21



	0.5T _{max}					
	0.5(T ₁ + T ₂)					
	0.5T ₁					
S _A	0	1	1	1	1	0
S _B	0	0	1	1	0	0
S _C	1	1	1	1	1	1

Fig. 22

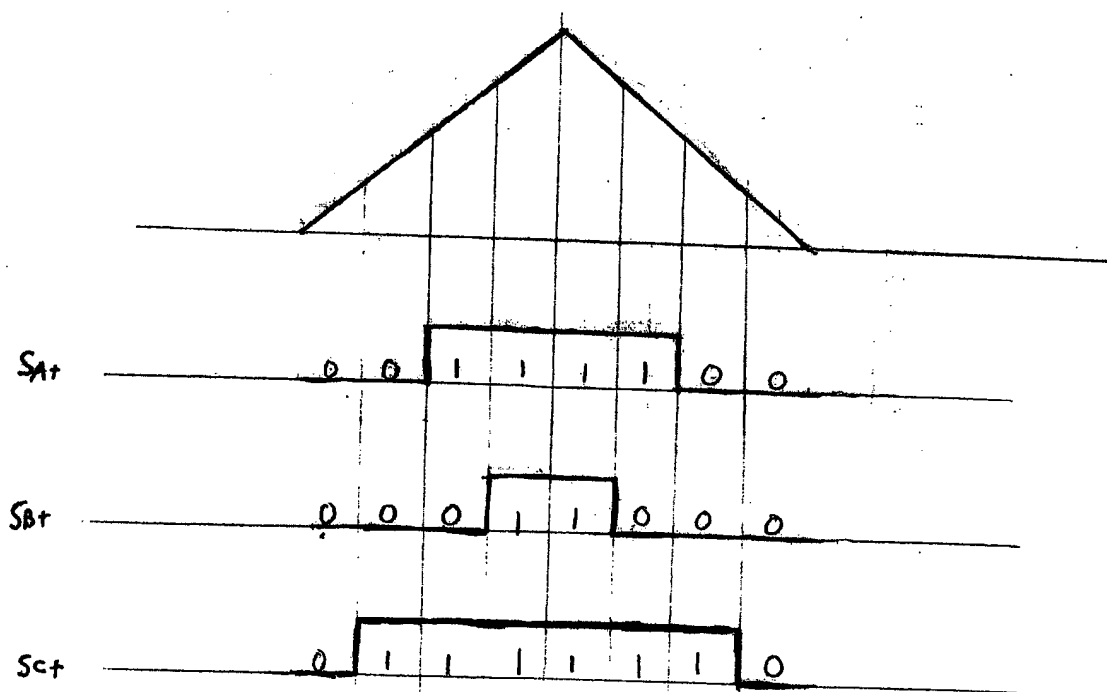


Fig. 22a

Output Converter Overload Rating

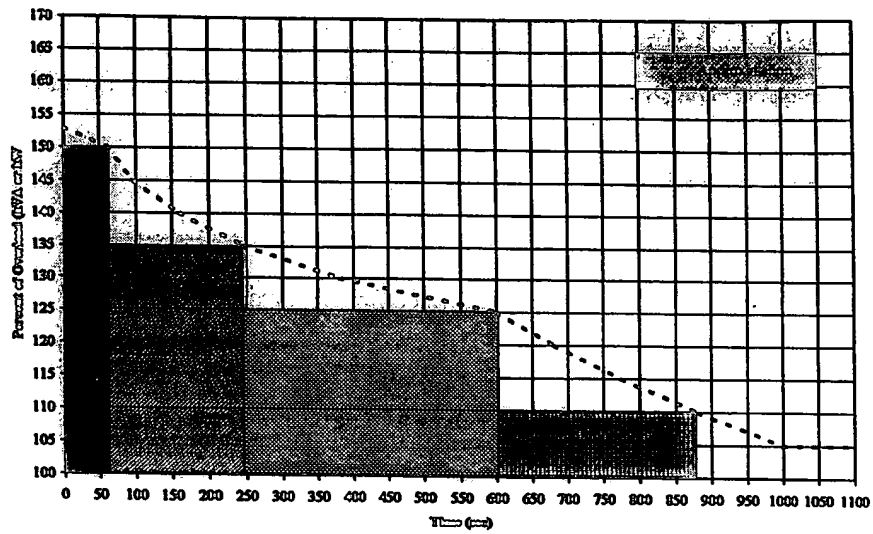


Fig. 23

Equivalent Watt - Seconds as Computed from Overload Characteristic Curve

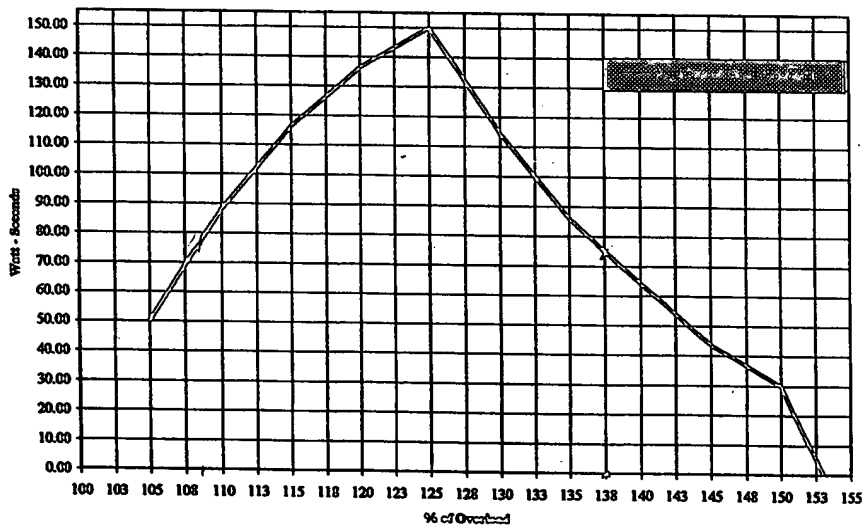


Fig. 24

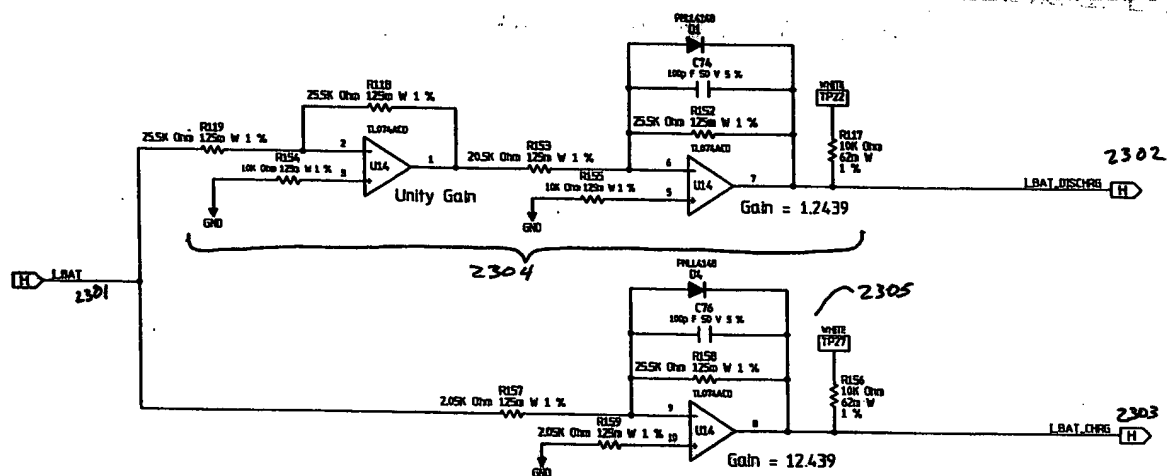


Fig. 25

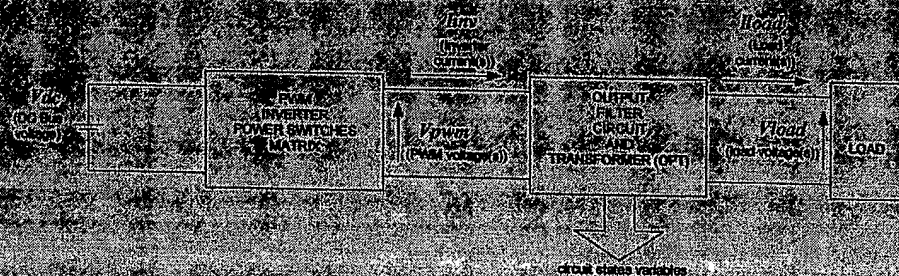


Fig. 26

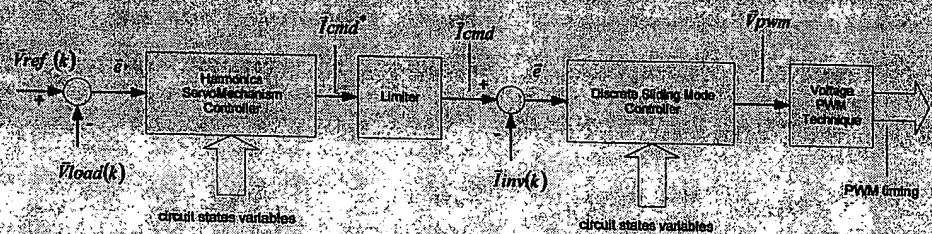


Fig. 27

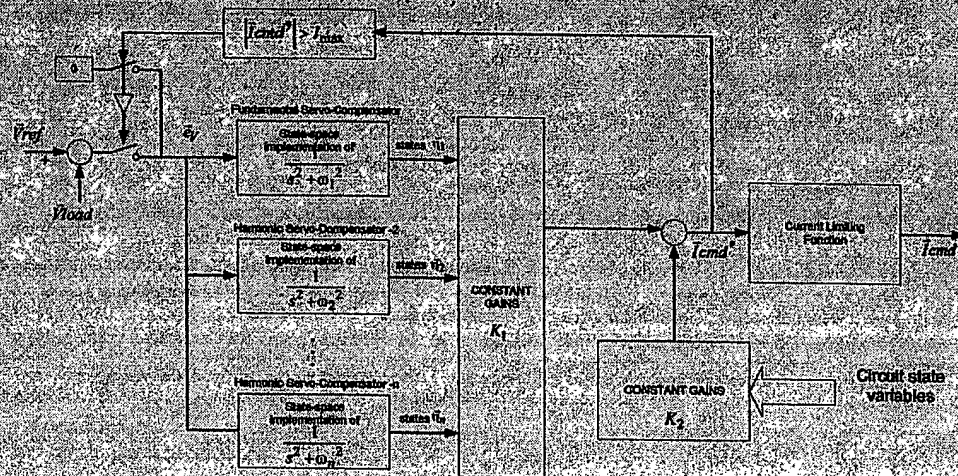


Fig. 28

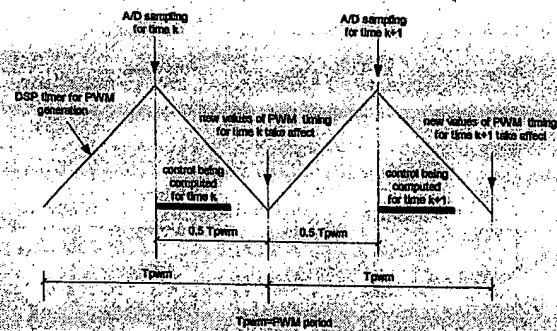


Fig. 29

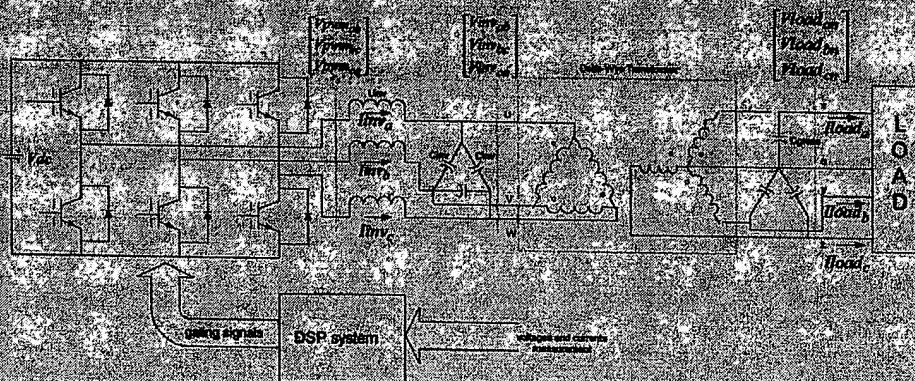


Fig. 30

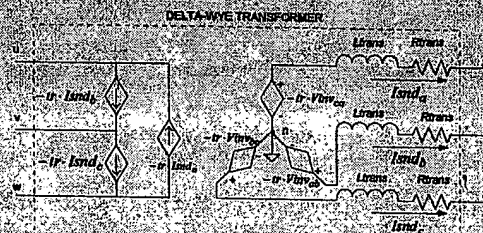


Fig. 31

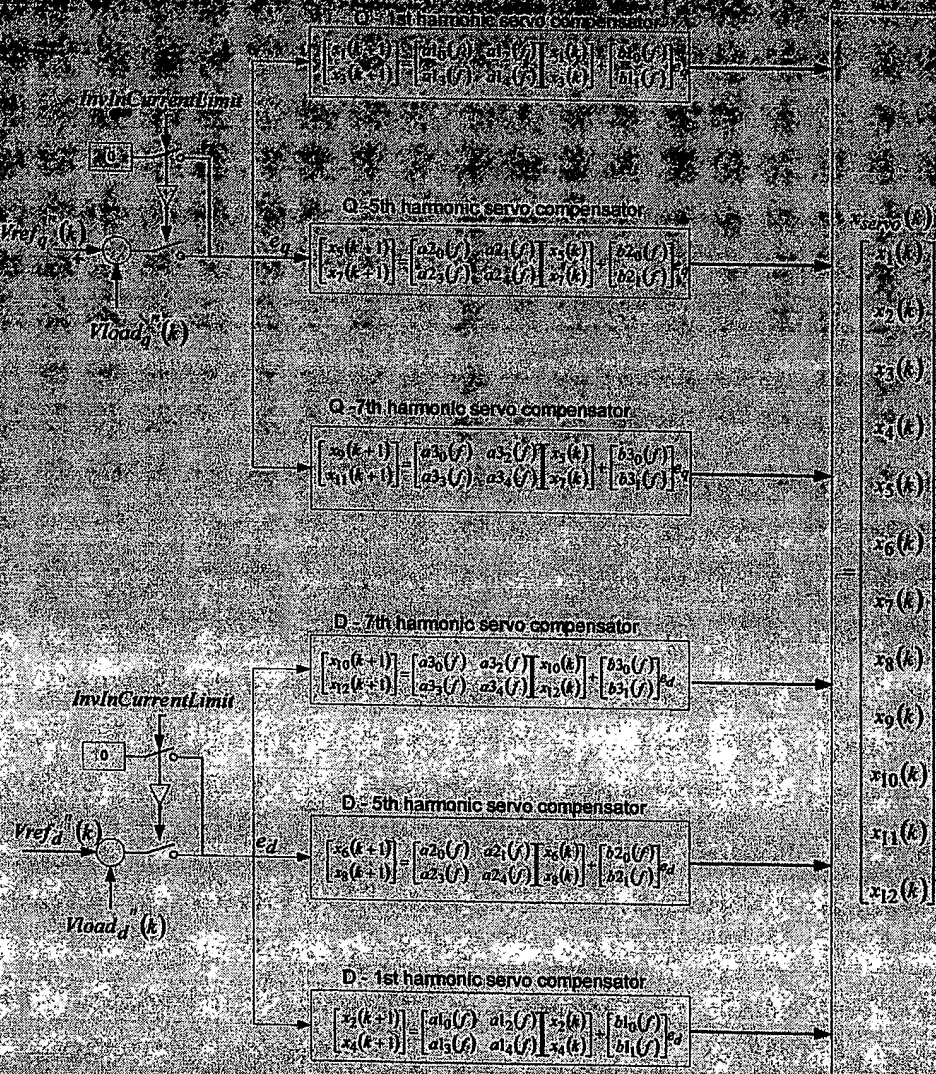


Fig. 32 Discrete time servo compensator

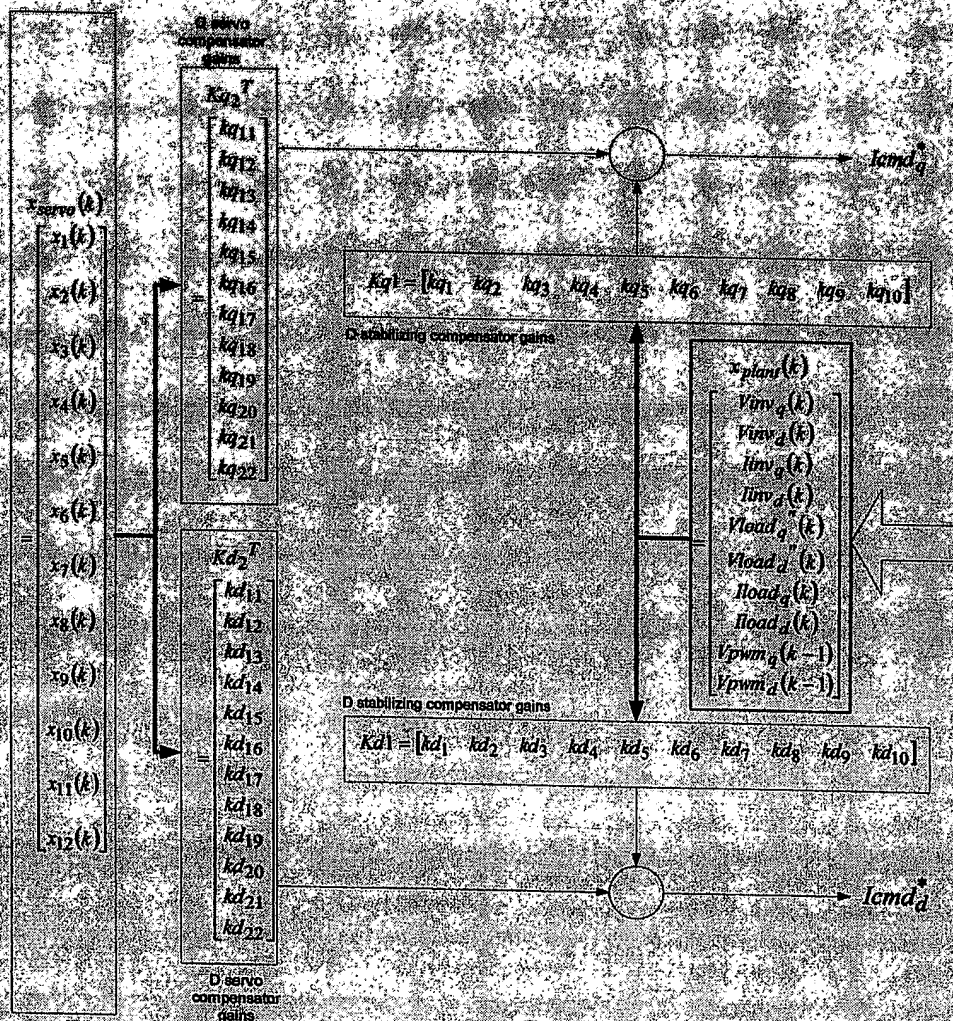
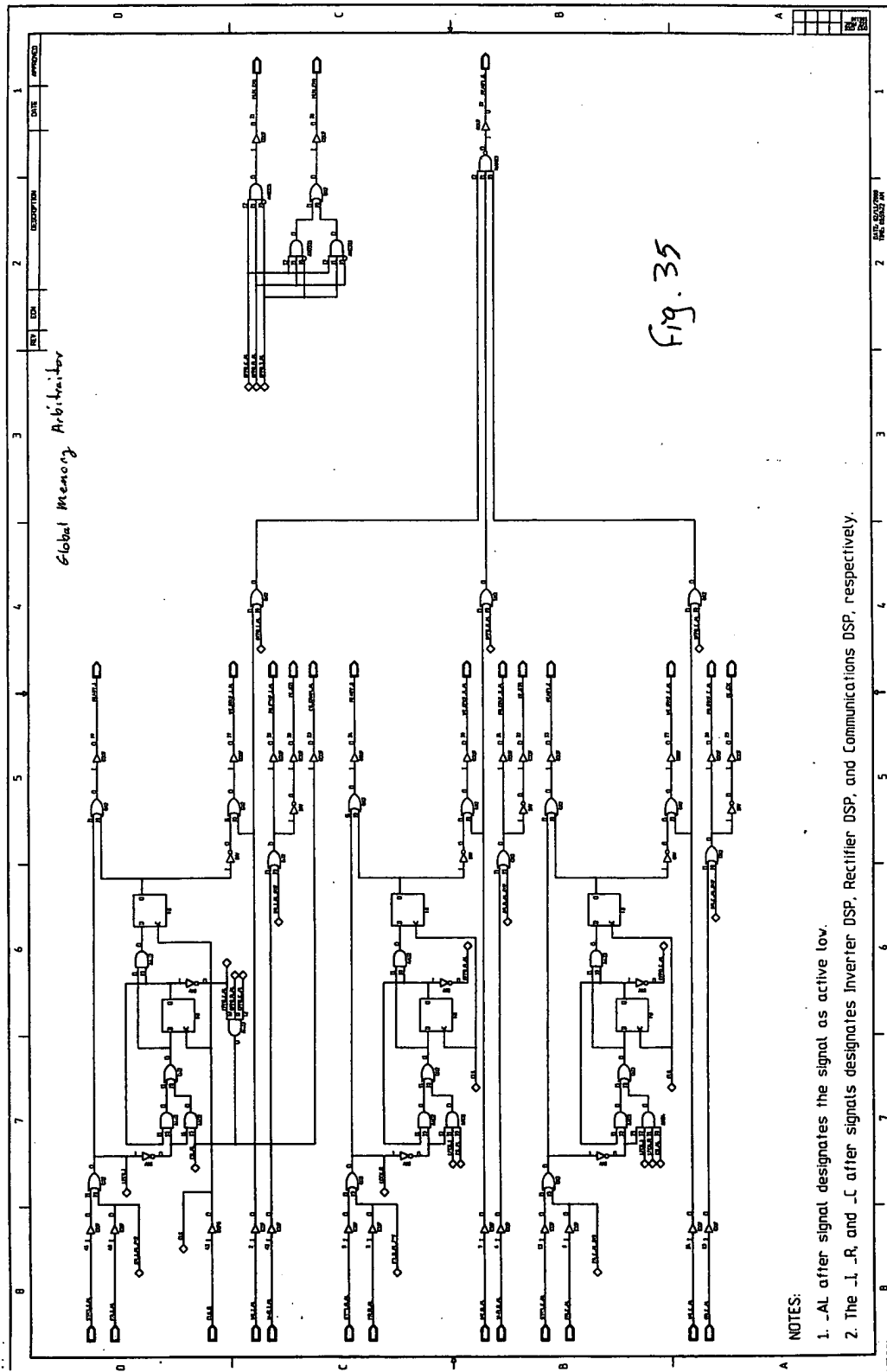


Fig. 33 Discrete time servo compensator (cont.)

TABLE 2-3-1



Fragmentation service is available from the receiver. A delay is performed to allow the receiver to respond.

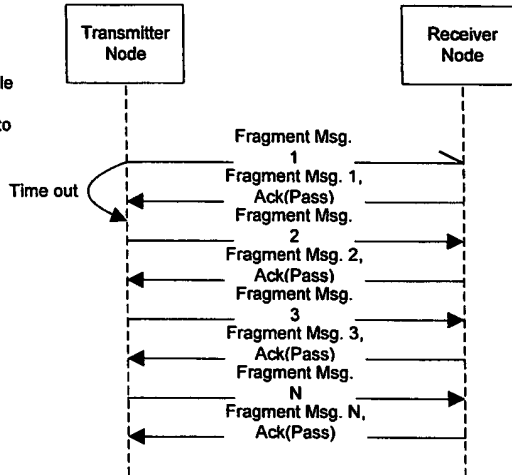


Fig. 36

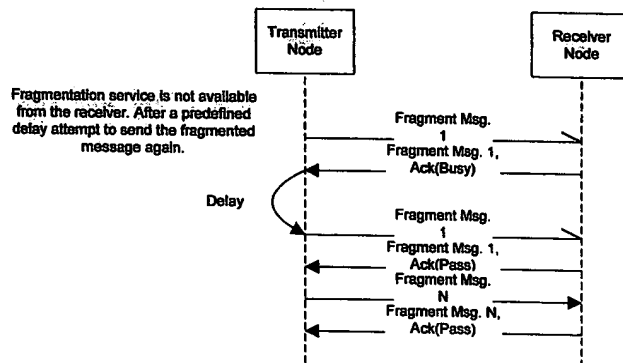


Fig. 37

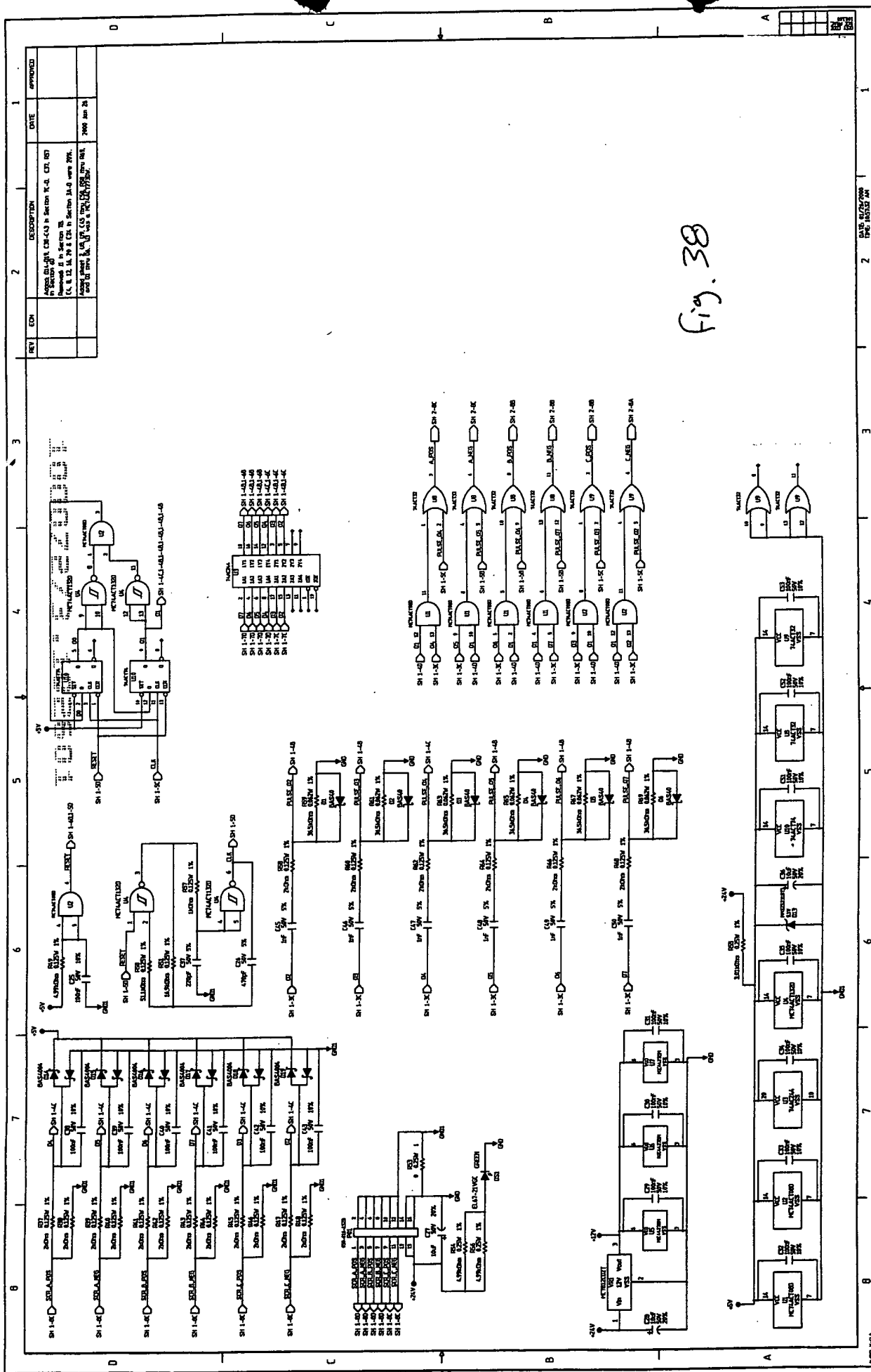


Fig. 38